

Abstract

A digital to analog converter is implemented by two or more digital to analog converters and is further implemented in such a way that the operation of its analog output voltages is combined and that its digital input voltages are processed in such a way that, in the case of a continuous incrementing or decrementing of the input values, the individual input values of the two or more digital to analog converters are incremented or decremented in turn. The invention furthermore relates to a phase control circuit in which the digital to analog converter can be used, and also a recognition circuit for recognizing the locking-in of a phase control circuit and a transmission unit in which such assemblies are used.